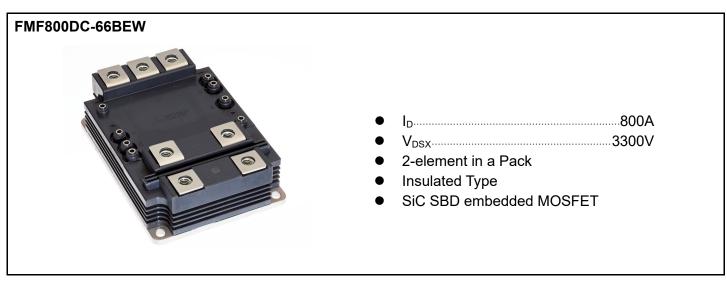


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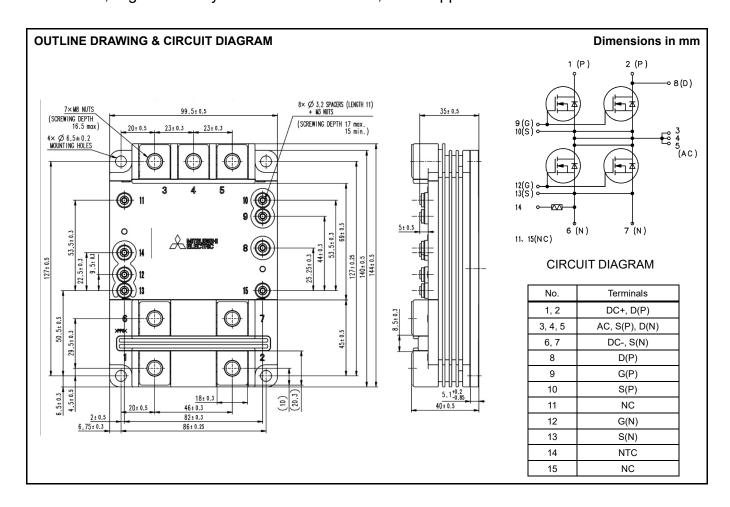
HIGH POWER SWITCHING USE

INSULATED TYPE 2nd gen. HVMOSFET (High Voltage Metal Oxide Semiconductor Field Effect Transistor) Modules



APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers



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HIGH POWER SWITCHING USE

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MAXIMUM RATINGS

Item	Symbol	Condition			Unit
Drain-Source voltage, specified gate-source voltage	V _{DSX}	V _{GS} = -7 V	3300	٧	
Gate-Source voltage	V_{GSS}	V _{DS} = 0 V	±20	٧	
Drain current	I _D	V_{GS} = 17 V , T_c = 87 °C , AC terminal output current (Note 1)	800	Α	
Drain current	I _{DP}	Non repetitive pulse	1600	Α	
Reverse drain current (FWD forward current)	Is	V_{GS} = -7 V , T_c = 85 °C , AC terminal output current(Note 1)(Note 2)		800	
Reverse drain current (FWD forward current)	I _{SP}	Non repetitive pulse(Note 2)	$T_j = T_{op}$	1600	Α
Total power dissipation	P _{tot}	T _c = 25 °C , MOSFET part(Note 3)	6650	W	
Isolation voltage	V _{isol}	Charge part to the baseplate RMS sinusoidal, 60Hz 1min			
Partial discharge charge	Charged part to the baseplate harge Q _{pd} RMS sinusoidal, 60 Hz 1min V1 = 3500 V, V2 = 2600 V(acc. to IEC 61287-1)				рС
Junction temperature	Tj	Maximum temperature range in off-state or on-state(non-switching)	-40~175	°C	
Case temperature	T _c	Maximum case temperature range in on-state	-40~150	°C	
Storage temperature	T _{stg} Maximum case temperature range in off-state				°C
Operating junction temperature	g junction temperature $T_{ m jop}$ Maximum junction temperature range for switching operation				°C
Short-circuit withstand pulse duration	t _{pSC}	V_{DD} = 2500 V , V_{GS} = +17 / -7 V , L_s = 40 nH , V_{GS} 50%- V_{GS} 50% T_j = T_{op}			μs
Short circuit energy	E _{sc}	V_{DD} = 2500 V , F(t)weibull=1% T_{j} = T_{op}		35	J
Non-repetitive surge forward current	I _{FSM}	t_p = 10ms, T_j = 175 °C T_j = 175 °C			kA
12t value	l ² t	t_p = 10ms, $ T_j = 175 \ ^{\circ}\text{C} $ F(t)weibull=1%, Half sinewave			kA ² s

ELECTRICAL CHARACTERISTICS

Item Symbol		Condition		Limits			Unit
				Min.	Тур.	Max.	5
Gate-source leakage current	I _{GSS}	$V_{DS} = 0 \text{ V}$, $V_{GS} = V_{GSS}$	T _j = 25 °C	-2.0	-	2.0	μΑ
			T _i = 25 °C	-	0.003	-	mA
Drain-source cut-off current	I _{DSX}	$V_{DS} = V_{DSX}$, $V_{GS} = -7 V$	T _j = 150 °C	-	0.050	-	mA
			T _i = 175 °C	-	0.080	3.0	mA
			T _i = 25 °C	1.60	2.10	2.60	V
Gate-source threshold voltage	$V_{GS(th)}$	$V_{DS} = 10 \text{ V}$, $I_{D} = 80 \text{mA}$	T _i = 150 °C	-	1.50	-	V
			T _i = 175 °C	0.90	1.45	1.90	V
		V _{DS} = V _{DS(on)} , V _{GS} = 17 V , P-side(Terminal 8-10)	T _i = 25 °C	-	2.00	-	mΩ
			T _i = 150 °C	-	4.31	-	mΩ
			T _i = 175 °C	-	5.00	6.06	mΩ
Drain-source on resistance	r _{DS(on)}	$V_{DS} = V_{DS(on)}$, $V_{GS} = 17 \text{ V}$, N-side(Terminal 10-13),(Note 5)	T _i = 25 °C	-	2.25	-	mΩ
			T _i = 150 °C	-	4.63	-	mΩ
			T _i = 175 °C	-	5.38	6.44	mΩ
Drain-source on-state voltage		I _D = 800 A , V _{GS} = 17 V , P-side(Terminal 8-10)(Note 4)	T _i = 25 °C	-	1.60	-	V
			T _i = 150 °C	-	3.45	-	V
	.,		T _i = 175 °C	-	4.00	4.85	V
	V _{DS(on)}	I _D = 800 A , V _{GS} = 17 V , N-side(Terminal 10-13),(Note 4))(Note 5)	T _i = 25 °C	-	1.80	-	V
			T _i = 150 °C	-	3.70	-	V
			T _i = 175 °C	-	4.30	5.15	V

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HIGH POWER SWITCHING USE

INSULATED TYPE 2nd gen. HVMOSFET (High Voltage Metal Oxide Semiconductor Field Effect Transistor) Modules

ELECTRICAL CHARACTERISTICS (continuation)

14	Courada ad	Condition			Limits		I limit
Item	Symbol	Condition		Min.	Тур.	Max.	Unit
			T _i = 25 °C	-	1.45	-	V
		I _S = 800 A , V _{GS} = 17 V , P-side(Terminal 8-10)(Note 4)	T _i = 150 °C	_	3.25	-	V
			T _i = 175 °C	-	3.80	4.40	V
Source-drain voltage	$V_{SD(on)}$		T _i = 25 °C	_	1.65	-	V
		I _S = 800 A , V _{GS} = 17 V , N-side(Terminal 10-13),(Note 4))(Note 5)	T _i = 150 °C	-	3.50	-	v
		15 000 11, 165 11 1 , 11 0140 (1011111111111 10 10),(11010 1),(11010 0)	T _i = 175 °C	_	4.10	4.70	v
			T _i = 25 °C	+-	2.00	4.70	V
		$I_{\rm S} = 800 \rm A$, $V_{\rm GS} = 0 \rm V$, P-side(Terminal 8-10)(Note 4)	T _i = 150 °C	_	3.85		v
			T _i = 175 °C	-	4.35	5.00	V
Source-drain voltage	V_{SD}		T _i = 25 °C	-	2.20	-	V
		I _S = 800 A , V _{GS} = 0 V , N-side(Terminal 10-13),(Note 4))(Note 5)	T _i = 150 °C	+ -	4.10	-	V
		11s - 500 A , V _{GS} - 0 V , 14-side(Terminal 10-15),(140te 4))(140te 5)		+		F 20	<u> </u>
			T _j = 175 °C	-	4.65	5.30	V
			T _i = 25 °C	-	2.00	<u> </u>	V
		I _S = 800 A , V _{GS} = -7 V , P-side(Terminal 8-10)(Note 4)	T _i = 150 °C	-	3.85	-	V
Source-drain voltage	$V_{SD(off)}$		T _j = 175 °C	-	4.35	5.00	V
g-	- SD(011)		T _i = 25 °C	-	2.20	-	V
		I_S = 800 A , V_{GS} = -7 V , N-side(Terminal 10-13),(Note 4))(Note 5)	T _j = 150 °C	-	4.10	-	V
			T _j = 175 °C	-	4.65	5.30	V
Input capacitance	C _{iss}	V_{DS} = 10 V , V_{GS} = 0 V , f = 100kHz , 1/2 module	T _j = 25 °C	-	110	-	nF
Output capacitance	Coss	V_{DS} = 10 V , V_{GS} = 0 V , f = 100kHz , 1/2 module	T _j = 25 °C	-	70	-	nF
Reverse transfer capacitance	C _{rss}	V _{DS} = 10 V , V _{GS} = 0 V , f = 100kHz , 1/2 module	T _i = 25 °C	-	2.7	-	nF
Gate charge	Q_G	V_{DD} = 1800 V , I_{D} = 800 A , V_{GS} = +17 / -7 V , 1/2 module	T _i = 25 °C	-	3.3	-	μC
Turn-on delay time	t _{d(on)}	V _{DD} = 1800 V , I _D = 800 A , V _{GS} = +17 / -7 V , L _s = 40 nH	T _i = 175 °C	-	-	0.50	μs
Rise time	t,	V_{DD} = 1800 V , I_{D} = 800 A , V_{GS} = +17 / -7 V , L_{s} = 40 nH	T _i = 175 °C	-	-	0.30	μs
	E _{on(10%)}	V_{DD} = 1800 V , I_{D} = 800 A , V_{GS} = +17 / -7 V , L_{s} = 40 nH $R_{G(on)}$ = 1.5 Ω , $R_{G(off)}$ = 1.5 Ω , Inductive load	T _i = 25 °C	_	0.26	-	J
Turn-on (switching) energy per pulse 10% integral			T _i = 150 °C	-	0.22	-	J
, , , , , , , , , , , , , , , , , , , ,	011(1070)		T _i = 175 °C	_	0.22	-	J
	E _{on}	V_{DD} = 1800 V , I_{D} = 800 A , V_{GS} = +17 / -7 V , L_{s} = 40 nH $R_{G(on)}$ = 1.5 Ω , $R_{G(off)}$ = 1.5 Ω , Inductive load	T _i = 25 °C	-	0.27	-	J
Turn-on (switching) energy per pulse			T _i = 150 °C	_	0.23	-	J
Turn on (switching) chargy per palse			T _i = 175 °C	_	0.23	-	J
			T _i = 25 °C	<u> </u>	10.7	-	μC
Total capacitive charge		$V_{DD}=1800~V~,~I_D=800~A~,~V_{GS}=+17~/~7~V~,~L_s=40~nH$ $R_{G(on)}=1.5~\Omega~,~R_{G(off)}=1.5~\Omega~,~Inductive~load$		+ -	12.5	-	μC
Total capacitive charge	$Q_{\mathbb{C}}$		T _j = 150 °C	+-	12.5	-	μC
			T _i = 175 °C T _i = 25 °C	_	0.004	_	J
Diada turn off aparau (par pulsa)	_	V_{DD} = 1800 V , I_{D} = 800 A , V_{GS} = +17 / -7 V , L_{s} = 40 nH $R_{G(on)}$ = 1.5 Ω , $R_{G(off)}$ = 1.5 Ω , Inductive load		-		-	
Diode turn-off energy (per pulse)	E _{off_Diode(10%)}		T _i = 150 °C	-	0.005	-	J
			T _j = 175 °C	-	0.005	-	J
		$V_{DD} = 1800 \text{ V}$, $I_{D} = 800 \text{ A}$, $V_{GS} = +17 / -7 \text{ V}$, $L_{s} = 40 \text{ nH}$	T _j = 25 °C	-	0.004	-	J
Diode switching off energy of diode	E _{off_Diode}	$R_{G(on)}$ = 1.5 Ω , $R_{G(off)}$ = 1.5 Ω , Inductive load	T _j = 150 °C	-	0.006	-	J
			T _j = 175 °C	-	0.006	-	J
Turn-off delay time	$t_{d(off)}$	$V_{DD} = 1800 \text{ V}$, $I_D = 800 \text{ A}$, $V_{GS} = +17 / -7 \text{ V}$, $L_s = 40 \text{ nH}$	T _j = 175 °C	-	-	0.94	μs
Fall time	t_f	$V_{DD} = 1800 \text{ V}$, $I_D = 800 \text{ A}$, $V_{GS} = +17 / -7 \text{ V}$, $L_s = 40 \text{ nH}$	T _i = 175 °C	-	-	0.40	μs
	E _{off(10%)}	V_{DD} = 1800 V , I_D = 800 A , V_{GS} = +17 / -7 V , L_s = 40 nH $R_{G(on)}$ = 1.5 Ω , $R_{G(off)}$ = 1.5 Ω , Inductive load	T _j = 25 °C	-	0.10		J
Turn-off (switching) energy per pulse 10% integral			T _j = 150 °C	-	0.11	-	J
			T _j = 175 °C	-	0.11	-	J
		V = 4000 V I = 000 A V = :47 / 7 V I = 40 ° V	T _i = 25 °C	-	0.10	-	J
Turn-off (switching) energy per pulse	E _{off}	$V_{DD} = 1800 \text{ V}$, $I_{D} = 800 \text{ A}$, $V_{GS} = +17 / -7 \text{ V}$, $L_{s} = 40 \text{ nH}$	T _i = 150 °C	-	0.11	-	J
, 3, 3, 1 1		$R_{G(on)}$ = 1.5 Ω , $R_{G(off)}$ = 1.5 Ω , Inductive load	T _i = 175 °C	-	0.11	-	J

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HIGH POWER SWITCHING USE

INSULATED TYPE 2nd gen. HVMOSFET (High Voltage Metal Oxide Semiconductor Field Effect Transistor) Modules

THERMAL CHARACTERISTICS

Item	Symbol	Conditions		Limits		
Item Symbol		Conditions		Тур.	Max.	Unit
Thermal resistance junction to case	$R_{\text{th(j-c)}}$	Junction to Case, MOSFET + embeded SBD part, 1/2 module	ı	-	22.5	K/kW
Contact thermal resistance case to heatsink	R _{th(c-s)}	Case to heat sink, λ_{grease} = 1W/m·K, $D_{(c-s)}$ = 70 μ m, 1/2 module	ı	22.5	-	K/kW

MECHANICAL CHARACTERISTICS

Item	Symbol	ol Conditions		Limits		
iteiii Sy		Conditions		Тур.	Max.	Unit
Mounting torque	$M_{\rm t}$	Main terminal screw M8 This is the case when installing the product on the bus bar		-	22.0	N∙m
Mounting torque	M _t	Mounting screw M6	3.0	-	6.0	N⋅m
Mounting torque	M _t	Auxiliary terminals screw M3	0.4	-	0.8	N·m
Mass	m	-	-	0.8	-	kg
Comparative tracking index	CTI	-	600	-	-	-
Clearance distance in air	d _a	Between main terminal	8.0	-	-	mm
Creepage distance along surface	d_s	-	32.0		-	mm
Internal inductance, D-S	L _{PDS}	Between DC+ and DC-(terminal1,2-6,7)	-	15	1	nH
	L _{PDS}	Between DC+ and AC (terminal1,2-3,4,5)	-	43		nH
	L _{PDS}	Between AC and DC-(terminal3,4,5-6,7)	-	43		nΗ
	R _{DD'+SS'}	T _C =25°C, Between DC+ and DC-(terminal1,2-6,7)	-	0.46	-	mΩ
Internal lead resistance, DD'-SS'	R _{DD'+SS'}	T _C =25°C, Between DC+ and AC(terminal1,2-3,4,5)	-	0.22	ı	mΩ
	R _{DD'+SS'}	T _C =25°C,Between AC and DC-(terminal3,4,5-6,7)	-	0.33	1	mΩ
Zero-power resistance	R ₂₅	T _C =25°C	4.65	5.00	5.35	kΩ
B-constant	B _(25/50)	Approximate by equation,(Note 6)	-	3375	-	K

Note 1. Control Case Temperature (Tc) so that the junction temperature (Tj) does not exceed the maximum rating.

Note 5. N-side characteristic value includes wiring resistance between P-side source potential and N-side drain potential. (P-side characteristic value corresponds to chip characteristics).

Note 6.
$$B_{(25/50)} = \ln(\frac{R_{25}}{R_{50}}) / (\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

 R_{25} : resistance at absolute temperature T_{25} [K]; T_{25} = 25[°C] + 273.15 = 298.15[K] R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} = 50[°C] + 273.15 = 323.15[K]

Products falling under the subject item No. 2 (41) 3 of Appended Table 1 of the Export Trade Control Order.

Note 2. The symbols represent characteristics of the anti-parallel, source to drain free-wheel diode (FWDi).

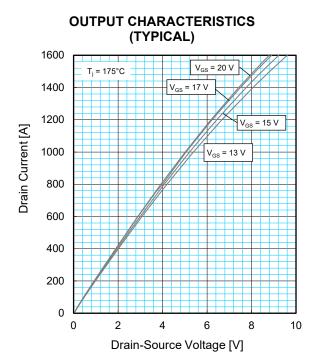
Note 3. Junction temperature (Tj) should not exceed Tjmax rating.

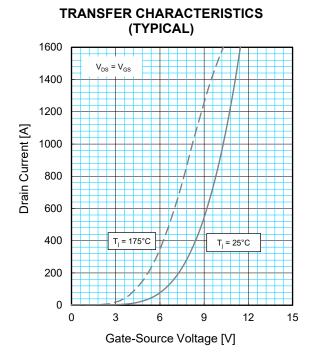
Note 4. Pulse width and repetition rate should be such as to cause negligible temperature rise.

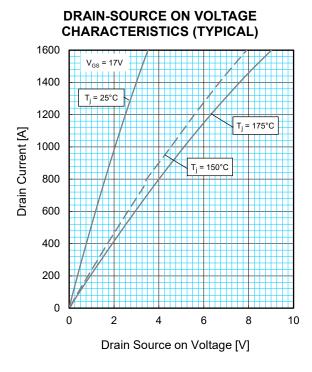
HIGH POWER SWITCHING USE

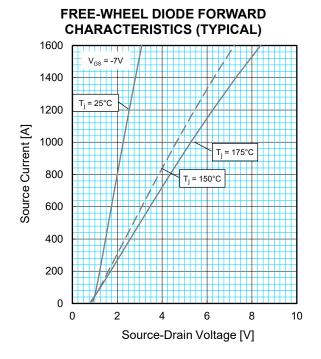
INSULATED TYPE 2nd gen. HVMOSFET (High Voltage Metal Oxide Semiconductor Field Effect Transistor) Modules

PERFPRMANCE CURVES







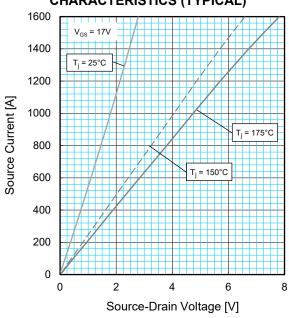


HIGH POWER SWITCHING USE

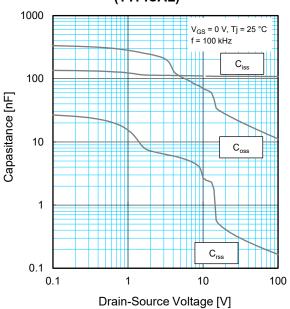
INSULATED TYPE 2nd gen. HVMOSFET (High Voltage Metal Oxide Semiconductor Field Effect Transistor) Modules

PERFORMANCE CURVES

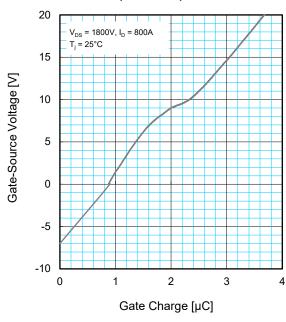




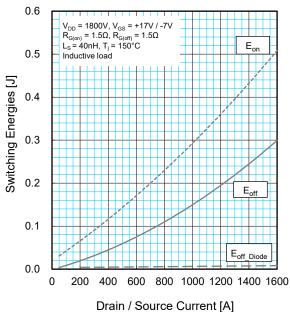
CAPACITANCE CHARACTERISTICS (TYPICAL)



GATE CHARGE CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

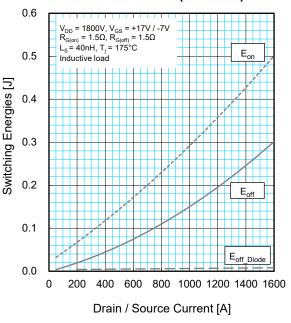


HIGH POWER SWITCHING USE

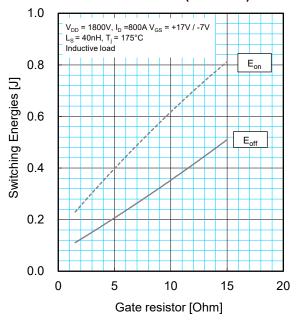
INSULATED TYPE 2nd gen. HVMOSFET (High Voltage Metal Oxide Semiconductor Field Effect Transistor) Modules

PERFORMANCE CURVES

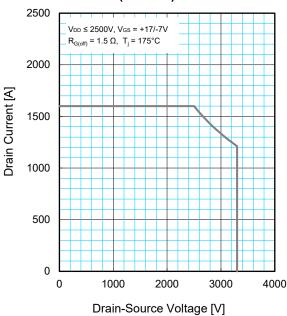
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



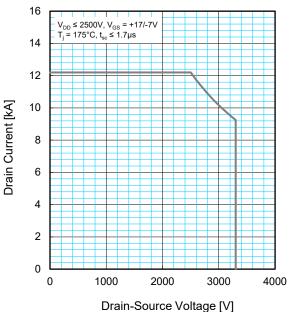
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



REVERSE BIAS SAFE OPERATING AREA (RBSOA)



SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)

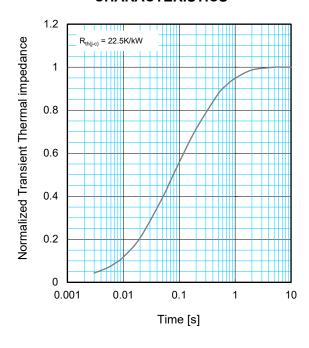


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INSULATED TYPE 2nd gen. HVMOSFET (High Voltage Metal Oxide Semiconductor Field Effect Transistor) Modules

PERFORMANCE CURVES

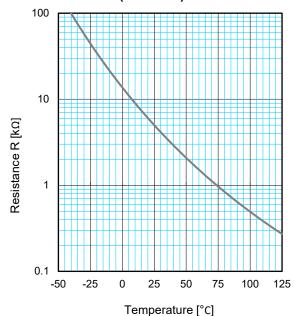
TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS



$$Z_{th(j-c)}(t) = \sum_{i=1}^{n} R_{i} \left\{ 1 - exp^{\left(-\frac{t}{\tau_{i}}\right)} \right\}$$

	1	2	3	4
R _i /R _{th} :	0.0078	0.1975	0.3553	0.4393
τ _i [sec.] :	0.0001	0.7324	0.0381	0.1698

NTC THERMISTOR TEMPERATURE CHARACTERISTICS (TYPICAL)



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HIGH POWER SWITCHING USE

INSULATED TYPE 2nd gen. HVMOSFET (High Voltage Metal Oxide Semiconductor Field Effect Transistor) Modules

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